nRF905 RF and antenna layout

1. General
Gerber files for RF layouts have been made for Nordic Semiconductor’s nRF905 Single Chip 433/868/915MHz RF Transceiver [1]. A loop antenna for 433MHz has also been made available.

The Gerber files for each layout are compressed into the Zip archive format. The Zip archive files includes a file named readme.wri that must be read before importing the Gerber files into the PCB editor.

All described layouts should be fabricated on standard 1.6mm double-sided FR4 printed circuit board.

2. RF layout for nRF905
The RF layouts include all necessary circuitry to design the radio part of a short-range communication system based on nRF905. All digital data and control signals are available at the perimeter of the RF layout and should be connected to the digital part of a customer’s application.

Supply voltage and GND are also available at the perimeter of the RF layout. The supply voltage for the RF part of the application circuit should be filtered separately from the supply voltages of any digital circuitry. Star routing is strictly recommended from the voltage supply source to the RF, digital or other parts of the application circuit.

All RF layouts presented below are equal except for the antenna connection circuitry. Solutions for differential connection to a differential antenna and single ended connection to a 50Ω antenna are presented.

2.1. Differential connection to a differential antenna
Figure 1 shows the schematics for RF layout with differential connection to a differential antenna. The PCB layout is shown in Figure 2. The loop antenna layout described in chapter 3 can easily be placed together with this RF layout. The connection points numbered 1, 2 and 3 on the loop antenna layout must be connected exactly to the corresponding connection points numbered 1, 2 and 3 on the RF layout.

The Gerber files Zip archive filename is PCB_nRF905_diff_0603_1-1.zip.

The recommended external components are as given in Table 1 for all frequency bands.
Figure 1. **nRF905** schematics for RF layout with differential connection to a differential antenna

- a) Top silk screen
- b) Bottom silk screen
- c) Top view
- d) Bottom view

Figure 2. **nRF905** RF layout with differential connection to a differential antenna, all frequency bands
### Component Description | Size | Value | Tolerance | Units
---|---|---|---|---
C1 Capacitor ceramic, 50V, NP0, (Crystal oscillator) | 0603 | 22 | ±5% | pF
C2 Capacitor ceramic, 50V, NP0, (Crystal oscillator) | 0603 | 22 | ±5% | pF
C3 Capacitor ceramic, 50V, NP0, (PA supply decoupling) @ 433MHz | 0603 | 180pF | ±5% | pF
C4 Capacitor ceramic, 50V, X7R, (PA supply decoupling) @ 868/915MHz | 0603 | 33 | ±5% | pF
C5 Capacitor ceramic, 50V, NP0, (Supply decoupling) @ 433MHz | 0603 | 4.7 | ±10% | nF
C6 Capacitor ceramic, 50V, X7R, (Supply decoupling) @ 868/915MHz | 0603 | 10 | ±10% | nF
C7 Capacitor ceramic, 50V, X7R, (Supply decoupling) @ 433MHz | 0603 | 10 | ±5% | pF
C8 Capacitor ceramic, 50V, X7R, (Supply decoupling) @ 868/915MHz | 0603 | 10 | ±5% | pF
R1 Resistor. 0.1W, (Crystal oscillator bias) @ 433MHz | 0603 | 1 | ±5% | MΩ
R2 Resistor. 0.1W, (Reference bias) @ 868/915MHz | 0603 | 22 | ±1% | kΩ
U1 nRF905 RF Transceiver QFN32L/5x5
X1 Crystal, $C_L = 12pF$, ESR $< 100Ω$, $C_0 < 7.0pF$ | LxWxH = 4.0x2.5x0.8mm | 16 1) | < +/- 30 ppm (frequency tolerance at 25°C + temperature drift) | MHz

1) nRF905 can operate at several crystal frequencies, please refer to [1].

Table 1. nRF905 recommended external components for all frequency bands
2.2. Single ended connection to 50Ω antenna by using a differential to single ended matching network

Figure 3 shows the schematics for nRF905 with single ended connection to 50Ω antenna by using a differential to single ended matching network when operating at 433MHz. The PCB layout is shown in Figure 4. Figure 5 shows the schematics for nRF905 with single ended connection to 50Ω antenna by using a differential to single ended matching network when operating at 868/915MHz. The PCB layout is shown in Figure 6.

The antenna connection point should be as close as possible to the output of the matching network. If this is not possible of practical reasons, the PCB track between the output of the matching network and the antenna connection should be carried out as a 50Ω microstrip line. In such a case, the length of the microstrip line is limited to a few centimeters. For a standard FR4 printed circuit board with 1.54mm substrate thickness and relative dielectric constant $\varepsilon_r \approx 4.45-4.25$ at 433MHz-915MHz, the width of the microstrip line should be 3mm.

For 433MHz operating nRF905, the Gerber files Zip archive filename is PCB_nRF905_single_netw_0603_433_1-0.zip.

For 868/915MHz operating nRF905, the Gerber files Zip archive filename is PCB_nRF905_single_netw_0603_868-915_2-0.zip.

The recommended external components are as given in Table 1, with addition of the components in the differential to single ended matching network as given in Table 2.
<table>
<thead>
<tr>
<th>Component</th>
<th>Description</th>
<th>Size</th>
<th>Value</th>
<th>Tolerance</th>
<th>Units</th>
</tr>
</thead>
<tbody>
<tr>
<td>C9</td>
<td>Capacitor ceramic, 50V, NP0, (Impedance matching) at 433MHz</td>
<td>0603</td>
<td>18</td>
<td>±5%</td>
<td>pF</td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td>5.6</td>
<td>&lt;±0.25pF</td>
<td></td>
</tr>
<tr>
<td>C10</td>
<td>Capacitor ceramic, 50V, NP0, (Impedance matching) at 433MHz</td>
<td>0603</td>
<td>18</td>
<td>±5%</td>
<td>pF</td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td>5.6</td>
<td>&lt;±0.25pF</td>
<td></td>
</tr>
<tr>
<td>C11</td>
<td>Capacitor ceramic, 50V, NP0, (Impedance matching) at 433MHz</td>
<td>0603</td>
<td>optional 4)</td>
<td>±0.25%</td>
<td>pF</td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td>Not used</td>
<td></td>
<td></td>
</tr>
<tr>
<td>C12</td>
<td>Capacitor ceramic, 50V, NP0, (Impedance matching) at 433MHz</td>
<td>0603</td>
<td>6.8</td>
<td>±5%</td>
<td>pF</td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td>22</td>
<td>±5%</td>
<td></td>
</tr>
<tr>
<td>C13</td>
<td>Capacitor ceramic, 50V, NP0, (Impedance matching) at 433MHz</td>
<td>0603</td>
<td>optional 5)</td>
<td>±5%</td>
<td>pF</td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td>4.7</td>
<td>&lt;±0.25pF</td>
<td></td>
</tr>
<tr>
<td>L1</td>
<td>Chip inductor, (Impedance matching) at 433MHz: SRF&gt;433MHz @ 868/915MHz</td>
<td>0603</td>
<td>12</td>
<td>±5%</td>
<td>nH</td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td>12</td>
<td>±5%</td>
<td></td>
</tr>
<tr>
<td>L2</td>
<td>Chip inductor, (Impedance matching) at 433MHz: SRF&gt;433MHz @ 868/915MHz</td>
<td>0603</td>
<td>39</td>
<td>±5%</td>
<td>nH</td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td>10</td>
<td>±5%</td>
<td></td>
</tr>
<tr>
<td>L3</td>
<td>Chip inductor, (Impedance matching) at 433MHz: SRF&gt;433MHz @ 868/915MHz</td>
<td>0603</td>
<td>39</td>
<td>±5%</td>
<td>nH</td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td>12</td>
<td>±5%</td>
<td></td>
</tr>
</tbody>
</table>

Table 2. **nRF905** recommended components in the differential to single ended matching network, all frequency bands

3) To achieve more reproducible values of matching network insertion loss in mass production and hence less variation in output power and sensitivity, components with tighter tolerances can be utilized.

4) This capacitor is optional for 433MHz operating **nRF905** and may be needed to tune the resonance frequency of the matching network if a printed circuit board which is different from a 1.6mm, 2 layer, FR4 printed circuit board is used.

5) This capacitor is optional for 433MHz operating **nRF905** and may be needed in some designs to reduce harmonic emissions when measuring directly at the matching network output.
Figure 3. 433MHz operating nRF905 schematic, single ended connection to 50Ω antenna by using a differential to single ended matching network

No components in bottom layer

a) Top silk screen
b) Bottom silk screen

c) Top view
d) Bottom view

Figure 4. nRF905 RF layout for 433MHz operation, single ended connection to 50Ω antenna by using a differential to single ended matching network, all frequency bands
Figure 5. 868/915MHz operating nRF905 schematic, single ended connection to 50Ω antenna by using a differential to single ended matching network

No components in bottom layer

a) Top silk screen
b) Bottom silk screen

c) Top view
d) Bottom view

Figure 6. nRF905 RF layout for 868/915MHz operation, single ended connection to 50Ω antenna by using a differential to single ended matching network, all frequency bands
3. Loop antenna layouts

A loop antenna with T-match [2] is a good solution for low cost and small size radio modules. A layout solution for a small rectangular loop antenna for 433MHz operation is described below.

Figure 7 shows the basic geometry of the designed rectangular loop antenna.

![Figure 7. Geometry of rectangular loop antenna](image)

The loop antenna physical parameters are

\[
\begin{align*}
  a_1 & = \text{loop antenna length [mm]} \\
  a_2 & = \text{loop antenna width [mm]} \\
  d & = \text{feed length [mm]} \\
  b & = \text{width of loop conductor}
\end{align*}
\]

The antenna is tuned to a resonance frequency of 433MHz with chip capacitors C1, C2 and C3. Capacitors C1, C2 and C3 should be NP0 type and have tolerances equal to or better than ±0.1pF. Capacitors C4 and C5 should also be NP0 type and have tolerances equal to or better than ±5%. The loop antenna is tuned to approximately 300Ω with a T-match. The width of the loop conductor is 1mm (40mil). There is no ground plane beneath the antenna.

Table 3 shows a summary of the designed loop antenna for 433MHz.

<table>
<thead>
<tr>
<th></th>
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<th></th>
<th></th>
<th></th>
</tr>
</thead>
<tbody>
<tr>
<td>35x20</td>
<td>22.5</td>
<td>3.9</td>
<td>±0.1</td>
<td>6.8</td>
<td>±0.1</td>
<td>4.7</td>
<td>±0.1</td>
<td>27</td>
<td>300</td>
</tr>
</tbody>
</table>

Table 3. Summary of the loop antenna for 433MHz operation

The component values given in Table 3 are only valid when the connection points numbered 1, 2 and 3 on the loop antenna layout, as shown in Figure 8, are connected exactly to the
corresponding connection points numbered 1, 2 and 3 on the RF layout with differential connection. The component footprints used in the loop antenna layout are size 0603. The layout of the rest of the communication system may influence the antenna tuning and require measurement of the antenna together with the complete system in order to find the values for $C_1$, $C_2$ and $C_3$ for your system.

![Loop antenna PCB layout](image)

Figure 8. Loop antenna PCB layout

It is recommended that a system should not be designed with a longer communication range than the application requires. Estimations on communication range with the antenna presented in this application note and a receiver with a given sensitivity and antenna gain, can be made based on the theory given in [2]. Initial communication range tests should be carried out with the lowest RF output power setting that, based on the estimations, satisfies the range requirements. If the achieved communication range does not satisfy the requirements, the RF output power should be increased to the required level.

The Gerber files Zip archive filename for the antenna layout is **Loop_35x20mm_433MHz_nRF9x5.zip**.

Measured gain of this 35x20mm loop antenna is approximately $-18$ dBi.
4. References

   Nordic Semiconductor ASA.
2. Application note nAN400-03, “Small loop antennas”, Nordic Semiconductor ASA.
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APPLICATION NOTE

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