### Description of change:

The nRF52832 revision 3 (build codes Gx0) introduces behavioral changes, as well as new/modified registers to the access port protection. All new registers and changes in functionality for revision 3 are documented in the nRF52832 product specification rev 1.7 and the errata document.

Customers who want to take advantage of the changes introduced in revision 3 will need to place orders via separate ordering codes (nRF52832-QFAA-G, nRF52832-QFAB-G and nRF52832-CIAA-G). Production of revision 2 (build code Ex0) is maintained and is ordered using the existing ordering codes (nRF52832-QFAA, nRF52832-QFAB and nRF52832-CIAA).

### Impact:

**Product:**

1. Form: ☒ No ☐ Yes – describe:
2. Fit: ☒ No ☐ Yes – describe:
3. Function: ☒ No ☐ Yes – describe: See detailed description below.
4. Quality or Reliability: ☒ No ☐ Yes – describe:

### Reason for change:

Informational Notice 133 (IN-133) identifies a fault injection technique that causes failure of the access port protection mechanisms on the nRF52 Series. The introduced changes to revision 3 seek to mitigate this known fault injection technique.

### Consequences of change:

In factory state, the nRF52832 revision 3 comes with the access port protection enabled. An ERASEALL command via the control access port (CTRL-AP) is required to enable access.

In order to lock the device debug port, execute the following steps to enable access port protection:

1. Start with a CTRL-AP ERASEALL operation.
2. Program code compiled with MDK 8.40.2 or later, with ENABLE_APPROTECT defined.
3. Write Enabled (0x00) to UICR.APPROTECT
4. Perform a hard reset to protect the device. The programmed code from step 2 will write APPROTECT.FORCEPROTECT to Force (0x00).

To unlock the device debug port (for debugging etc.), execute the following steps to disable access port protection:

1. Start with a CTRL-AP ERASEALL operation.
2. Program code compiled with an MDK 8.40.2 or later, without ENABLE_APPROTECT defined.
3. Write HwDisabled (0x5A) to UICR.APPROTECT
4. Perform any reset to run the code. The programmed code from step 2 will open access port by writing to APPROTECT.DISABLE during start-up.

If an nRF52832 revision 3 is programmed with software compiled with a version of the MDK prior to release 8.40.2, the debug port will be locked. However, the APPROTECT.FORCEPROTECT will not be written to Force (0x0), and the errata workarounds implemented in the MDK may not be applied to revision 3. Thus, it is required to upgrade to the latest MDK (version 8.40.2 or later) to ensure correct behavior.

### Tele regulatory and Bluetooth certification:

The described changes have no impact on the RF performance, which remains identical to revision 2.

### Verification of change:

The new build code was qualified according to Nordic Semiconductor’s standard product approval and quality procedures.

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**IN no.: IN-142 rev 1.1**

**Device affected:** nRF52832-QFAA, nRF52832-QFAB and nRF52832-CIAA

**Date:** 2021-10-11

**Device version / Build Code:** Ex0

**Data sheet references:** N/A

**Agreement reference:** N/A

**Customers reference:** N/A

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Marking/Shipping labels:

- DEVICE: NRFxxxxx-<PP><VV>-<CC>
- S/O No.: <Nordic Sales Order>
- QTY:<Quantity>
- <Box ID>
- Build Codes:
  - <H>: HW version
  - <P>: Production Cfg.
  - <F>: FW version
- DEVICE: NRFxxxxx-<PP><VV>-<CC>
- HPF
- S/O No.: <Nordic Sales Order>
- Build Codes:
  - <H>: HW version
  - <P>: Production Cfg.
  - <F>: FW version

Chips:
- N52832 QFAAG0 YYWLLL
- N52832 QFAAG1 YYWLLL
- N52832 QFABG0 YYWLLL
- N52832 CIAAG0 YYWLLL
- N52832 CIAAG1 YYWLLL

Labels:
- G00
- G10

Change active from (date):
Orders can be placed now.

Samples Available (date/build code):
2021-09-08 / Gx0

Attentions:  
- No
- Yes – describe:

Technical contact at Nordic Semiconductor:

Commercial contact at Nordic Semiconductor:
www.nordicsemi.com, “Contact Us”

Authorization for Nordic Semiconductor

Product Manager: Pär Håkansson Date: 2021-10-11 Sign:

Quality Director: Ebbe Rømcke Date: 2021-10-11 Sign:

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Page 2 of 2